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To be filled in by the candidate

...... Year/Semester Examinations, 20.....

Subject

2

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	Instructions to candidate		•
	Please read this instructions carefully before you start writing your answers.		
1.	This answerbook has 28 pages. Please check before writing whether the book is complete and in good condition.		
2.	Please furnish all the details asked for at the space provided in the main answer book and the additional answer book, if any. Writing this details anywhere else on the answer book will invalidate your examination		
3.	Do not write your name or the name of your College/ Institutions anywhere or anything else, which is not a part of		
	your answer.		
4.	Write legibly on both sides of the paper. Strating from		
5.	Page No. 1 You may use some space for your rough notes or	For Descriptive T Questions	
2.	calculations on the answer book if you so desire. These rough notes, calculations must be scored out before	Page No.	Ma
	submitting the answer book.		10-10-
6.	Do not bring any book or loose paper in the examination hall.		
7.	Do not tear any page from the Answer Book.		
8.	Do not write anything on the question paper or blotting paper or any pieces of paper while you are in the examination hall.		
9.	Any act of indiscipline or misbehaviour in the examination hall will result in your expulsion.		
10	No examinee is allowed to leave the examination hall until 30 (thirty) minutes lapse after the commencement of the examination.		
11.	Additional answer sheet will be supplied after the main answer book is exhausted.		
12.	Use of cell phone, calculator or any electronic devises in the examination hall is strictly prohibited unless specified in the Question Paper.		,
	Sorutinizar's Signature		

Invigilator's Signature

# 2020 (CBCS) (FOURTH SEMESTER) ELECTRONICS (Pulse Switching Circuits) Full Marks: 50 Time: 3 hours

#### Instructions:

- 1. Questions should be attempted as per instructions.
- 2. Do not copy the Questions. Indicate the Questions No. clearly while attempting the answer.
- 3. Multiple choice answer should indicate the Question No., Sub. No., (if any) and the correct answer. For example-
  - Name the state capital of Mizoram.
     (a) Lunglei (b) Aizawl (c) Champhai
     Candidate should provide answer as Q. No. 1: (b) Aizawl [Candidate

should avoid writing only (b)]

- 4. The figures in the margin indicate full marks for the questions.
- 5. Separate answer script should be used.

# (SECTION: A – OBJECTIVE)

(Marks: 10)

## Choose the correct answer from the following questions: $1 \times 10 = 10$

- 1. Negative feedback is employed in
  - (a) oscillators (b)rectifiers
  - (c) amplifiers (d)transducer
- 2. The gain of an amplifier with feedback is known as
  - (a) closed-loop gain (b)open-loop gain
  - (c) resonant gain (d)current gain
- 3. For sustaining oscillation in an oscilator
  - (a) No feedback (b) phase shift should be  $90^{\circ}$
  - (c) feedback should be negative (d) feedback factor should be unity
- 4. In a transistor Hartley oscillator(a) inductive feedback is used(b)untapped coli is used
  - (c) entire coil is in the output circuit (d)no capacitor is used

5.	. An important limitation of a crystal oscillator is				
	(a) its high Q	(b)its low output			
	(c) less availability of quartz c	rystal (d)its high output			
6.	In a Wien bridge oscillator, if the resistances in the positive feedback circuit are decreased, the frequency				
	(a) remains the same	(b)decreases			
	(c) increases	(d)insufficient data			
7.	A bistable multivibrator has (a) two stable states	(b) one stable state (d) three stable states			
	(c) no stable state	(d) three stable states			
8.	The frequency of oscillation of an astable multivibrator depends on the (a) value of transistor $\beta$ (b) value of collector load resistors				
	(c) RC values of the circuit	(d) width of the input pulse			
9.	The number 1000 <sub>2</sub> is equivalen (a) one thousand (c) four	t to decimal number (b) eight (d) sixteen			

10. In Boolean algebra, the bar sign (-) indicates.

- (a) OR operation (b) AND operation
- (c) NOT operation (d) none of the above

# (SECTION: B – SHORT NOTES) (Marks: 10)

# Answer any four questions of the following:

 $2^{1/2} \times 4 = 10$ 

**1.** Show that the application of negative feedback reduces distortion and noises in an amplifier

## Or

Why is feedback necessary in an amplifier? Draw a circuit diagram of negative feedback amplifier showing the components of feedback circuit.

**2.** What is oscillator? Distinguish between damped and undamped oscillator.

# Or

For the Hartley oscillator, C = 250 pF,  $L_1 = 1.5$  mH,  $L_2 = 1.5$  mH and M = 0.58 mH. Determine the operating frequency

**3.** Write the circuit diagram of phase-shift oscillator. Also write its advantages and disadvantages

#### Or

Discuss the condition for stability of oscillator.

- What is the basic difference among the three types of multivibrators? Or Write the uses of Schmitt trigger.
- 5. Divide  $1110011_2$  by  $101_2$  using binary division method. Or

Write the symbol and truth table of NOR gate

(SECTION: C – DESCRIPTIVE) (Marks: 30) The questions are of equal values

# Answer any *three* from the following questions:

## 10x3=30

- (a) What is feedback in an amplifier? With appropriate diagrams, explain the working of positive and negative feedback amplifiers.
  - (b) When negative voltage feedback is applied to an amplifier of gain 100, the overall gain falls to 50. (i) calculate the fraction of the output voltage feedback (ii) If this faction is maintained, calculate the value of amplifier gain required if the overall stage gain is to be 75.
    - (c) Why is negative feedback applied in high gain amplifiers?

# OR

- **2.** (a) Show that in a Negative Feedback Amplifier, the gain is stabilized.
  - (b) Explain different types of negative feedback with diagrams.

- **3.** (a) Derive the frequency of oscillation and condition for sustained oscillation of Colpitt's oscillator.
  - (b) Explain the circuit operation of tuned collector and derive its frequency of oscillation

## OR

- **4.** (a) Explain the constructions and operations of series-fed and shunt-fed Hartley oscillators.
  - (b) The tuned collector oscillator circuit used in the local oscillator of a radio receiver makes use of an LC tuned circuit with  $L_1 = 58.6 \,\mu\text{H}$  and  $C_1 = 300 \,\text{pF}$ . Calculate the frequency of oscillations.
  - (c) Find the operating frequency of a transistor Hartley Oscillator if  $L_1 = 50 \mu$ H,  $L_2 = 1$  Mh and mutual inductance M = 10  $\mu$ H and C = 10 pF.
- **5.** (a) With circuit diagram, explain the operation, advantages and disadvantages of Wien bridge oscillator.
  - (b) A crystal has a parameters L = 3.3 H, C = 0.65 pF, R = 5.5 k $\Omega$  and  $C_s = 10$  pF. Calculate (i) series resonant frequency (ii) parallel resonant frequency.
  - (c) In the phase shift oscillator,  $R_1 = R_2 = R_3 = 1M\Omega$  and  $C_1 = C_2 = C_3 = 68$  pF. At what frequency does the circuit oscillate?

## OR

- **6.** (a) What is piezo electric effect? Explain series and parallel resonant frequencies from crystal oscillator equivalent circuit.
  - (b) In the Wien Bridge oscillator,  $R_1 = R_2 = 220 \text{ k}\Omega$  and  $C_1 = C_2 = 250 \text{ pF}$ . Determine the frequency of oscillations?
  - (c) For a tunnel diode, L=1.0 $\mu$ H, and C= 40 pF. If the negative resistance region of the diode characteristics has a negative slope  $r_d = 200 \Omega$  and the bulk resistance of the device  $R_B = 25 \Omega$ , does the circuit produce oscillations?

- 7. (a) Explain the construction, operation of Astable multivibrator.
  - (b) Why is Schmitt Trigger called *emitter-coupled binary*? Write the uses of Schmitt Trigger.

## OR

- **8.** (a) With a neat sketch, explain the construction and operation of Monostable multivibrator.
  - (b) In the astable multivibrator shown below,  $R_2 = R_3 = 10 \text{ k}\Omega$  and  $C_1 = C_2 = 0.01 \mu\text{F}$ . Determine the time period and frequency of the square wave.



- **9.** (a) With the help of logic ciruit diagram, explain the operation and truth table of XOR gate.
  - (b) Write the symbol and truth table of AND gate
  - (c) Convert the binary fraction 0.101 into its decimal equivalent

# OR

- **10**. (a) Write the construction and working of half subtractor.
  - (b) Using 1's complemental method, subtract  $01101_2$  from  $11011_2$ .
  - (c) With logic circuit, obtain the truth table for a NAND and NOR gate.